Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	thyristor adj memory adj cell	USPAT	OR	OFF	2004/12/07 17:21
L2	2	thyristor adj memory adj cell	US-PGPUB; USPAT	OR	OFF	2004/12/07 17:21
L3	547	thyristor and temperature adj control	US-PGPUB; USPAT	OR	OFF	2004/12/07 17:22
L4	1	3 and memory adj cell	US-PGPUB; USPAT	OR	OFF	2004/12/07 17:23
L5	9	thyristor-based adj memory adj cell	US-PGPUB; USPAT	OR	OFF	2004/12/07 17:29
L6	9	365/180.ccls. and temperature	US-PGPUB; USPAT	OR	OFF	2004/12/07 17:29